In the Specification

Please replace the Abstract with the new abstract set forth below:

A method for generating at least one error checking code includes creating a modified input data word, including (k+m) bits, by combining an input data word, having n-bits, with at least a previously calculated at least one error checking code, including m-bits, where $n \leq k$, and calculating a first m-bit error checking code from at least a first selected portion of the modified input data word.